LOW POWER DUAL **OPERATIONAL AMPLIFIER**

The DBL358 consists of two independent high gain, internally frequency compensated operational amplifiers designed to operate from a single power supply over a wide range of voltage.

☐ FEATURES

- O Input common mode voltage range includes ground
- O Internally frequency compansated for unity gain
- O Large DC voltage gain: 100dB
- O Wide bandwidth for unity gain: 1MHz
- O Very low power consumption
- O Wide supply voltage range:

Single: 3V~30V Dual: $\pm 1.5V \sim \pm 15V$

☐ APPLICATIONS

- O Transducer amplifiers
- O DC gain blocks
- O Conventional operational amplifiers

Characteristic

MAXIMUM RATINGS

Differential Input Voltage

Operating Temperature

Storage Temperature

Supply Voltage

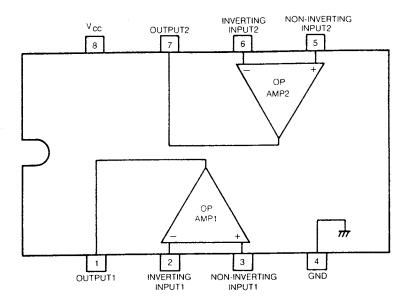
Input Voltage

Power Dissipation

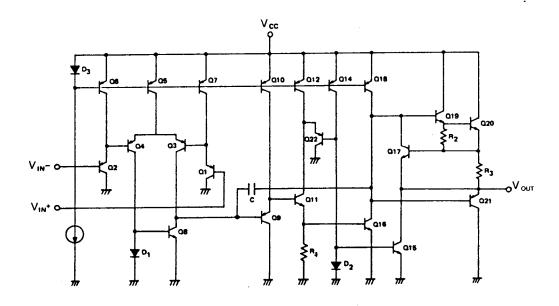
1 mw € 0 0 2 ± 0.1
Unit
V
V
V
mW
°C
°C

8DIP	Unit: mm			
100 PM 52 1 10 020 10 10 10 10 10 10 10 10 10 10 10 10 10	19,4%, 1 1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2			
8SOP	Unit:mm			
B 7 6 5 B 7 6 5 B 7 6 5 B 7 6 5 DBL2055 DBL2055 DBL2055 DBL2055 DBL2055 DBL2055 DBL2055	0.65 0.65 0.2±0.1			

☐ BLOCK DIAGRAM



☐ EQUIVALENT CIRCUIT (One section)



DBL 358

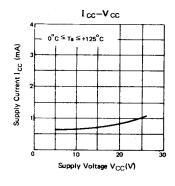
☐ ELECTRICAL CHARACTERISTICS

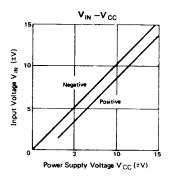
(Unless otherwise specified, V_{CC} =5V and $0^{\circ}C$ ${\leq}Ta$ ${\leq}70^{\circ}C$)

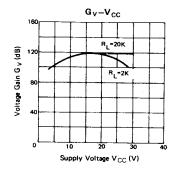
Characteristic	Symbol	Test Condition			Min.	Тур.	Мах.	Unit
		V _{OUT} = 1.4V, 5V≦V _{CC}	_⊃ ≦30V	Ta = 25°C	_	±2	土7	mV
Input Offset Voltage	V _{IOS}	$Rg = 0 \Omega$, $0V \le V_{IC} \le V_{CC} - 1.5V$					±9	mV
Input Offset Voltage Drift	△V _{IOS} /△T	$R_g = 0 \Omega$			_	7	_	μV/°C
1	_			Ta=25°C	_	±5	±50	nA
Input Offset Current	I ios					_	±150	nA
Input Offset Current Drift	$\Delta I_{IOS}/\Delta T$					10	_	pA/°C
Innut Bing Courses	т	Ta = 25°C			45	250	nA	
Input Bias Current	IIB					40	500	nA
Input Common Mode Voltage Range	V _{ICR}	V _{CC} =30V		0	_	Vcc- 1.5	٧	
				0	_	V _{CC} - 2	V	
		V _{CC} =30V, R _L =∞			_	1	2	mA
Supply Current	Icc	$V_{CC} = 5V, R_L = \infty$			_	0.7	1.2	mA
Large Signal		$V_{CC} = 15V, R_L \ge 2K \Omega$ $Ta = 25^{\circ}C$		25	100	_	V/mV	
Volage Gain	G _V			15	_	_	V/mV	
Output Voltage Swing	V _{ОН}	$V_{CC} = 30V$ $\frac{R_L = 2K \Omega}{R_L \ge 10K \Omega}$		26	_	_	٧	
				ΟΚΩ	27	28	_	٧
	V _{OL}	$V_{CC} = 5V$, $R_L \le 10K\Omega$				5	20	mV
Common Mode Rejection Ratio	CMRR	Ta = 25°C			65	70	_	dB
Power Supply Rejection Ratio	PSRR	Ta = 25°C			65	100	_	dB
Output Short Circuit to Ground	I os	Ta = 25°C			_	40	60	mA
Differential Input Voltage	V _{IND}	_			_		32	٧
Output Source Current		V _{CC} = 15V,		Ta = 25°C	20	40	_	mA
	I _O +	V _{IN} . = 1V, V _{IN} . = 0V		10	20	_	mA	
Output Sink Current		V _{CC} = 15V,		Ta = 25°C	10	20	_	mA
	I 0-	$V_{1N^{*}} = 0 V, V_{1N^{-}} = 1 V$		5	8	_	mA	
		V _{OUT} =200mV, Ta=25°C						
		$V_{IN^+} = 0V, V_{IN^-} = 1V$			12	50-	_	μΑ

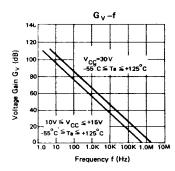
^{*} V_{IC} : Input Common Mode Voltage

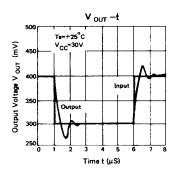
☐ TYPICAL PERFORMANCE CHARACTERISTICS

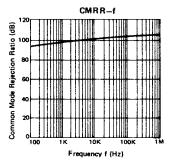


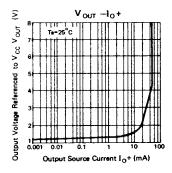


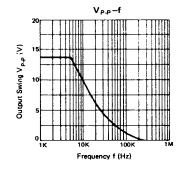


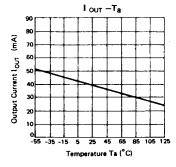












APPLICATION INFORMATION

The DBL358 operates with only a single power supply voltage, has true-differential inputs and remains in the linear mode with an input common mode voltage of 0V. This amplifier works over a wide range of power supply voltage with little change in performance characteristics. At 25°C amplifier operation is possible down to a minimum supply voltage of 2.3V.

Large differential input voltage can be easily accommodated and, as input differential voltage protection diodes are not used, no large input currents result from large differential input voltages. The differential input voltage may be larger than V_{CC} without damage. Protection should be provided to prevent the input voltage from going negative more than -0.3V. An input clamp diode with a resistor to the IC input terminal can be used.

To reduce the power supply current drain, the amplifier has a class A output stage for small signal level which converts to class B in a large signal mode. This allows the amplifiers to both source and sink large output currents.,

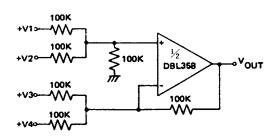
Therefore both NPN and PNP external current boost transistors can be used to extend the power capability of the basic amplifiers, the output voltage needs to raise approximately 1 diode drop above ground to bias the on-chip vertical PNP transistor for output current sinking applications.

Output short circuits either to ground or to the positive power supply should be of short time duration. Units can be destroyed, not as a result of the short circuit current causing metal fusing, but rather due to the large increase in IC chip dissipation which will cause eventual failure due to excessive junction Temperatures Putting direct shortcircuits on more than one amplifier at a time, the total IC power dissipation will increase to destructive levels, if not properly protected with external dissipation limiting resistors in series with the output leads of the amplifiers.

☐ APPLICATION CIRCUITS

(Single supply circuits, $V_{CC} = 5V$)

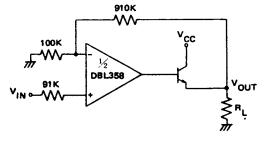
1. DC Summing Amplifier $(V_{IN'S} \ge 0V \& V_{OUT} \ge 0V)$



$$V_{OUT} = V1 + V2 - V3 - V4$$

 $(V1 + V2) \ge (V3 + V4)$ to keep $V_{OUT} > 0V$

2. Power Amplifier



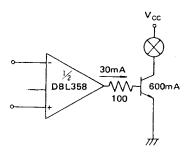
$$V_{OUT} = 0V \text{ for } V_{IN} = 0V$$

$$G_V = 10$$

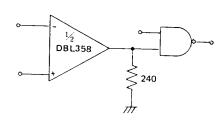
DBL 358

☐ APPLICATION CIRCUITS (continued)

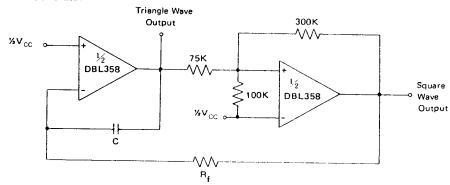
3. Lamp Driver



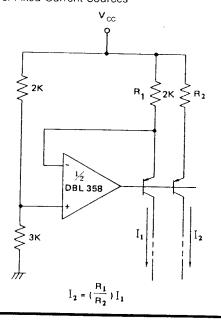
4. Driving TTL



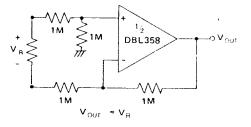
5. Function Generator



6. Fixed Current Sources



7. Ground Referencing a Differential input Signal



8. Pulse Generator 30K D₁

150K C D₁

DBL358

V_{CC} O NO 100K

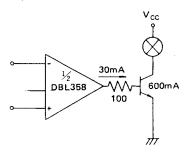
100K

C=0.01µF

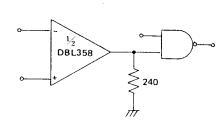
DBL 358

☐ APPLICATION CIRCUITS (continued)

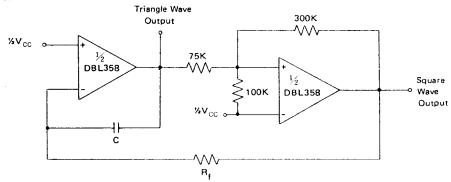
3. Lamp Driver



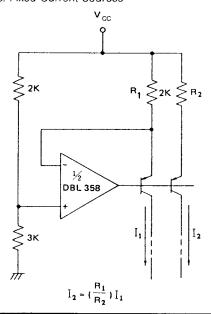
4. Driving TTL



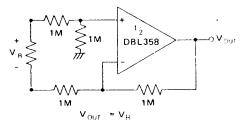
5. Function Generator

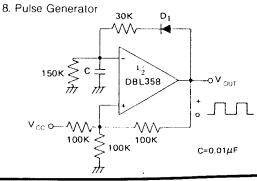


6. Fixed Current Sources



7. Ground Referencing a Differential input Signal





This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.