TOSHIBA

Discrete Semiconductors

2SK1118

Field Effect Transistor

Silicon N Channel MOS Type (π-MOS II)

High Speed, High Current DC-DC Converter,

Relay Drive and Motor Drive Applications

Features

- 4-Volt Gate Drive
- Low Drain-Source ON Resistance
- $R_{DS(ON)} = 0.95\Omega$ (Typ.)
- High Forward Transfer Admittance
- |Y_{fs}| = 4.0S (Typ.)
 Low Leakage Current
 - $-1_{DSS} = 300 \mu A (Max.) @ V_{DS} = 600V$
- Enhancement-Mode
- $V_{th} = 1.5 \sim 3.5 V @ V_{DS} = 10 V, I_D = 1 mA$

Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V _{DSS}	600	V
Drain-Gate Voltage ($R_{GS} = 20k\Omega$)		V _{DGR}	600	٧
Gate-Source Voltage		V _{GSS}	±30	۷
Drain Current	DC	ID	6	A
	Pulse	I _{DP}	24	
Drain Power Dissipation (Tc = 25°C)	•	PD	45	W
Channel Temperature		T _{ch}	150	°C
Storage Temperature Range		T _{stg}	-55 ~ 150	°C

Industrial Applications Unit in mm 10 ± 0.3 Ø3.2±0.2 2.7 ± 0.2 3.9 5.6 MAX 1.1 13.0MIN 0.75 ± 0.15 2.54 ± 0.25 2.54 ± 0.25 32 1. GATE 2. DRAIN 3. SOURCE JEDEC EIAJ SC-67 TOSHIBA 2-10R1B Weight : 1.9g

Thermal Characteristics

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	R _{th(ch-c)}	2.77	°C/W
Thermal Resistance, Channel to Ambient	R _{th(ch-a)}	62.5	°C/W

This transistor is an electrostatic sensitive device. Please handle with care.

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2/6

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Electrical Characteristics (Ta = 25°C)

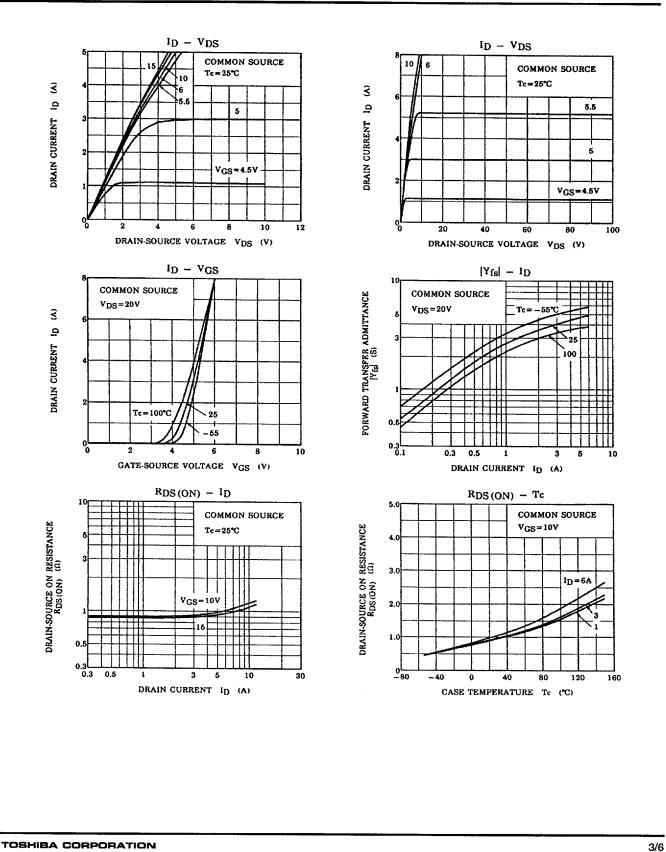
CHAR	ACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		I _{GSS}	$V_{GS} = \pm 25V, V_{DS} = 0V$	-	-	±100	nA
Drain Cut-off Current Drain-Source Breakdown Voltage		IDSS	I_{DSS} $V_{DS} = 600V, V_{GS} = 0V$	-	-	300	μA
		V(BR) DSS	$I_{D} = 10 mA, V_{GS} = 0V$	600	-	-	٧
Gate Threshold Voltage		Vth	V _{DS} = 10V, I _D = 1mA	1.5	-	3.5	v
Drain-Source ON Resistance		R _{DS (DN)}	$I_{D} = 3A, V_{GS} = 10V$	-	0.95	1.25	Ω
Forward Transfer Admittance		Y _{ts} I	$V_{DS} = 10V, I_D = 3A$	3.0	4.0		S
Input Capacitance Reverse Transfer Capacitance		Ciss	V _{DS} = 10V, V _{GS} = 0V, f = 1MHz		1400	2000	pF
		C _{rss}		-	75	120	
Output Capacitance		C _{oss}		-	250	380	
Switching Time	Rise Time	t _r	$V_{GS}^{10V} \qquad I_{D=3A} \\ V_{GS}^{10V} \qquad I_{D=3A} \\ V_{OUT} \qquad I_{OU} \\ V_{OUT} \qquad I_{OU} \\ V_{OU} \\ V$	-	25	50	ns
	Turn-on Time	t _{on}		-	40	80	
	Fall Time	t,		-	20	40	
	Turn-off Time	t _{off}		-	85	170	
			$V_{IN}: t_{r}, t_{f} < 5ns, V_{DD} = 300V$ Duty $\leq 1\%, t_{W} = 10\mu s$				
Total Gate Charge (Gate-Source Plus Gate-Drain)		Qg	$V_{DD} = 400V, V_{GS} = 10V, I_{D} = 6A$	-	56	110	nC
Gate-Source Charge		Q _{gs}		-	32	-	
Gate-Drain ("Miller") Charge		Q _{gd}	1	-	24	-	1

Source-Drain Diode Ratings and Characteristics (Ta = 25° C)

CHARACTERISTICS	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	IDR	_	-	_	6	A
Pulse Drain Reverse Current	IDRP	-	-		24	A
Diode Forward Voltage	V _{DSF}	$I_{DR} = 6A, V_{GS} = 0V$	-		-2.0	v
Reverse Recovery Time	t _{rr}	$I_{DR} = 6A, V_{GS} = 0V$	-	460	-	ns
Reverse Recovered Charge	Qrr	dl _{DR} /dt = 100A/µs	-	3.5	-	μC

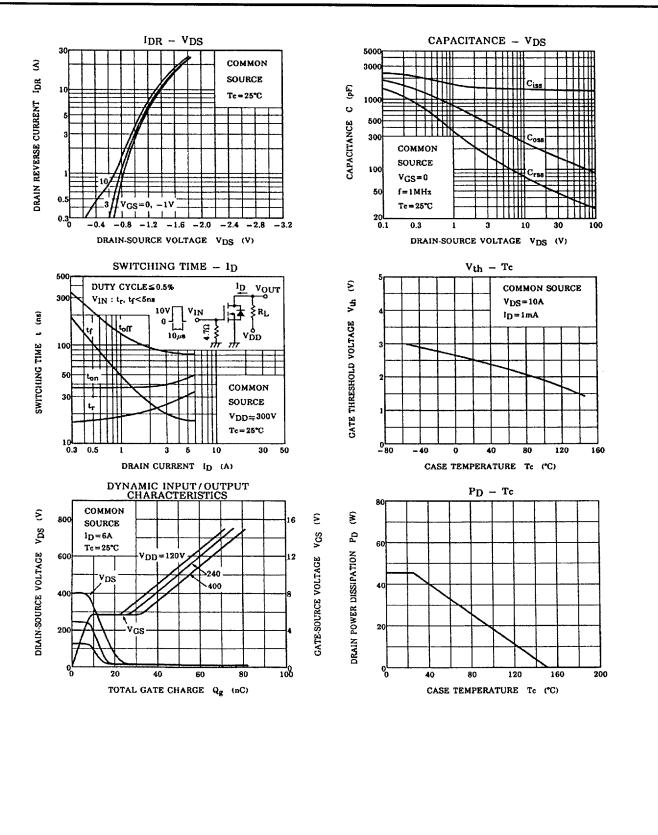
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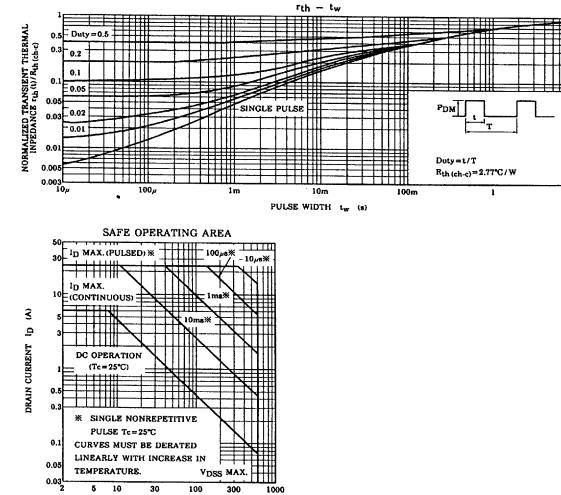
4/6



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10



DRAIN-SOURCE VOLTAGE VDS (V)

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5/6

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