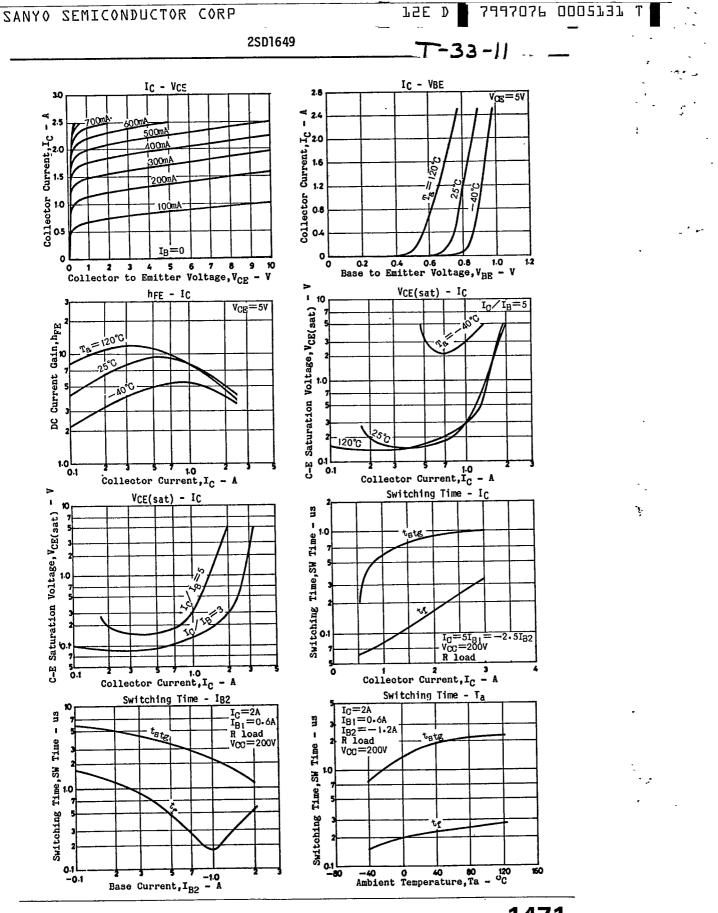
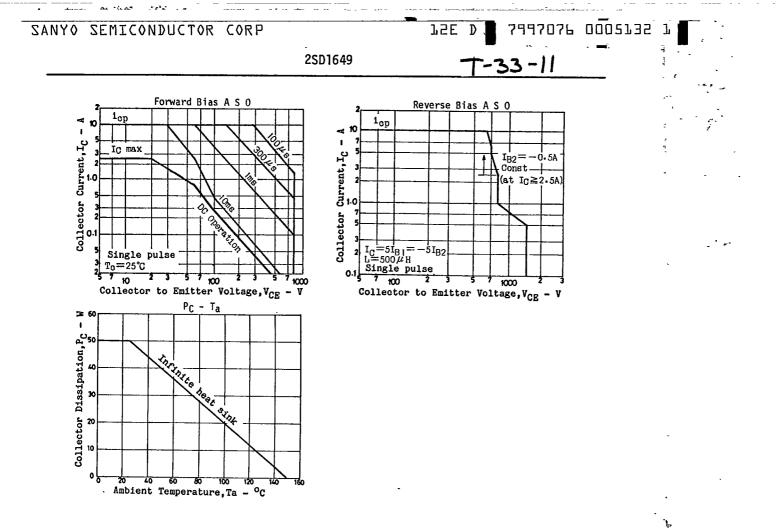
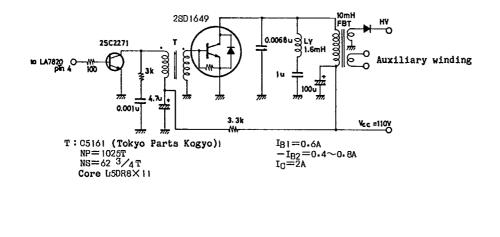
YO SEMICONDUCTOR CO	RP		LZE D 7997076 00	]05130
2SD1649			T-33-	າເ
	20		NPN Triple Diffused Planar Silicon Transistor	
	Color T	V Horizontal	Deflection Output	
©1755B		Applications(	with Damper Diode)	
Applications . High-voltage, power su	vitching	••		
Features Fast speed (t <sub>f</sub> max=0.4u High reliability (Adop High breakdown voltage Micaless package facil	otion of H (V <sub>CBO</sub> =15	00V).		
Absolute Maximum Ratings a Collector to Base Voltage Collector to Emitter Vol Emitter to Base Voltage Collector Current Peak Collector Current Collector Dissipation Junction Temperature Storage Temperature	se V <sub>CB</sub>	0 0 0 T <sub>C</sub> =25 <sup>0</sup> C	unit 1500 V 800 V 6 V 2.5 A 10 A 50 W 150 <sup>O</sup> C -55 to +150 <sup>O</sup> C	
Electrical Characteristics Collector Cutoff Current Emitter Cutoff Current DC Current Gain Gain Bandwidth Product Collector to Emitter Saturation Voltage Base to Emitter	, I <sub>CBO</sub> I <sub>EBO</sub> h <sub>FE</sub> f <sub>T</sub> V <sub>CE(sat)</sub>	V <sub>CB</sub> =800V,I <sub>E</sub> =0 V <sub>EB</sub> =4V,I <sub>C</sub> =0 V <sub>CE</sub> =5V,I <sub>C</sub> =0.5A V <sub>CE</sub> =10V,I <sub>C</sub> =0.5A	min typ max uni 10 uk 40 130 mk 8 3 MHz 8 V 8 V	4 1 7
Saturation Voltage Collector to Base Breakdown Voltage Collector to Emitter		I <sub>C</sub> =5mA,I <sub>E</sub> =0	1500 V	
Breakdown Voltage Emitter to Base Breakdown Voltage		$I_{C}$ =100mA, $R_{BE}$ = $\infty$ $I_{E}$ =200mA, $I_{C}$ =0	800 V 7 V	· ·
Diode Forward Voltage Fall Time	V <sub>F</sub> t <sub>f</sub>	$I_{EC}=2.5A$ $I_{C}=2A, I_{B1}=0.6A,$ $I_{B2}=-1.2A, V_{CC}=200W$ $R_{L}=1000hm$	2 V 0.4 us	
Switching Time Test Circui PW=20us.0uty≤1% 0UTPUT	Case	R <sub>L</sub> =1000nm <b>Outline</b> 2039 <u>80</u> t:nm) <u>50 - 1</u>	- 22.0	
			E: Emitt C: Colle C:	ctor
		5277K	C/D244KI,TS No.1755-1/3	





## Sample Application Circuit



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## SANYO SEMICONDUCTOR CORP

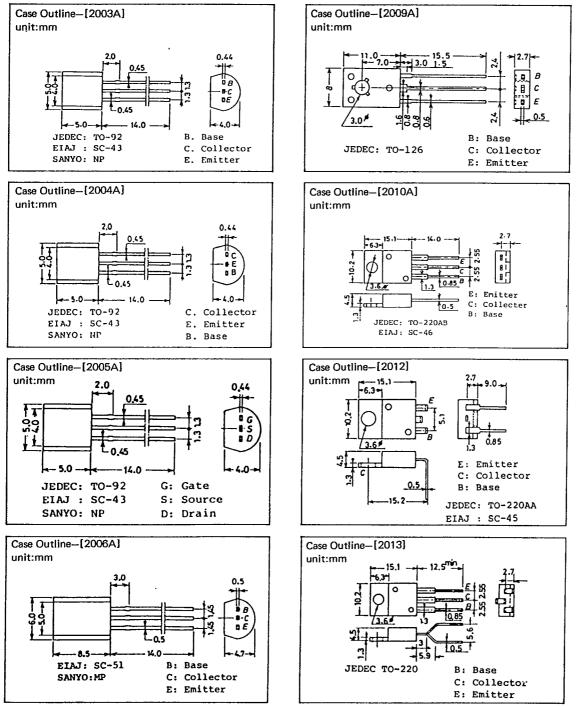
## 75E D 24.

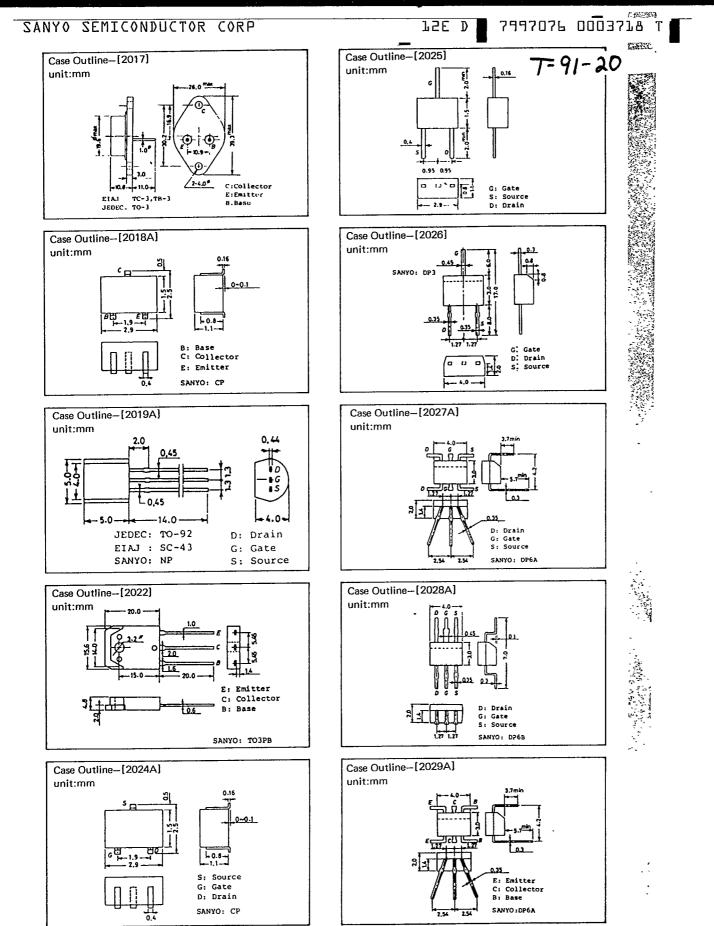
7997076 0003717 8

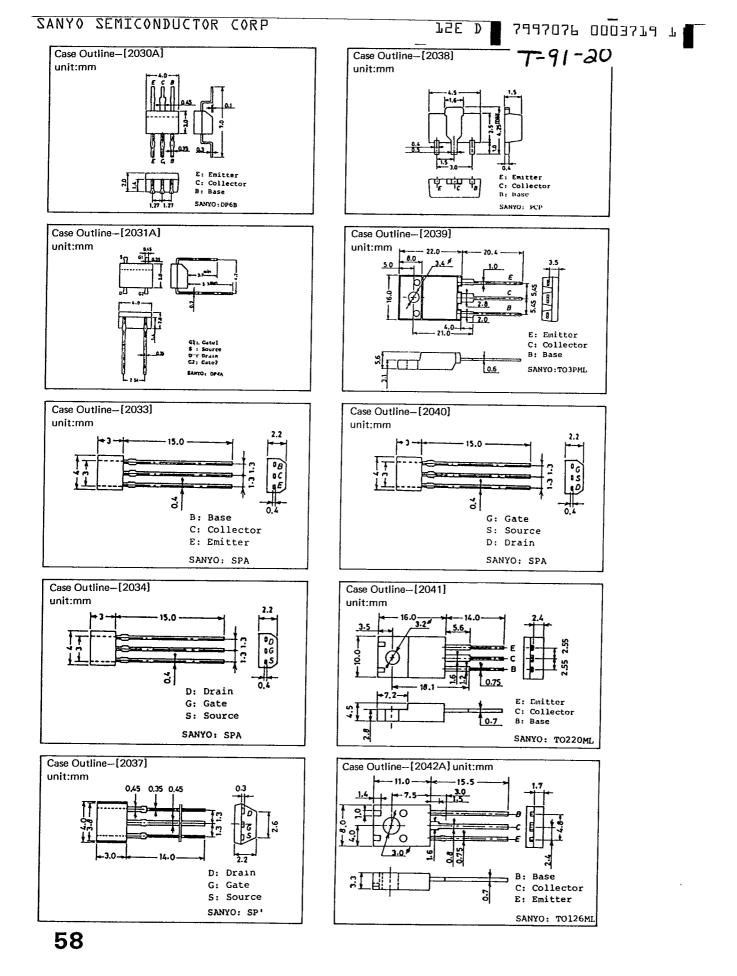
T-91-20

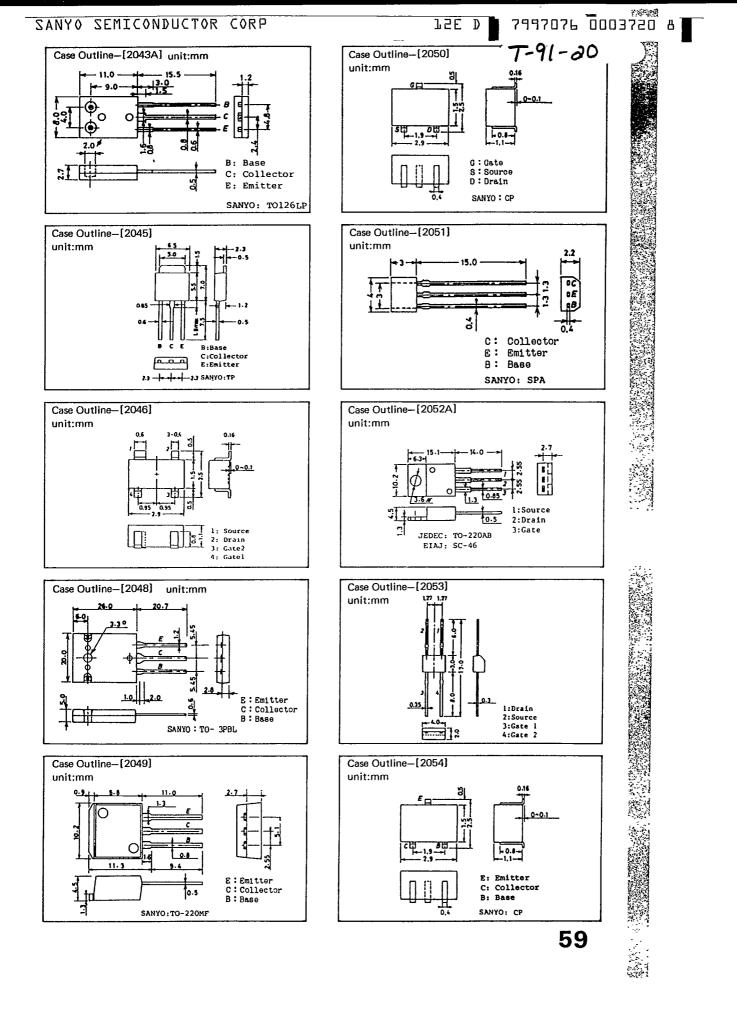
## CASE OUTLINES AND ATTACHMENTS

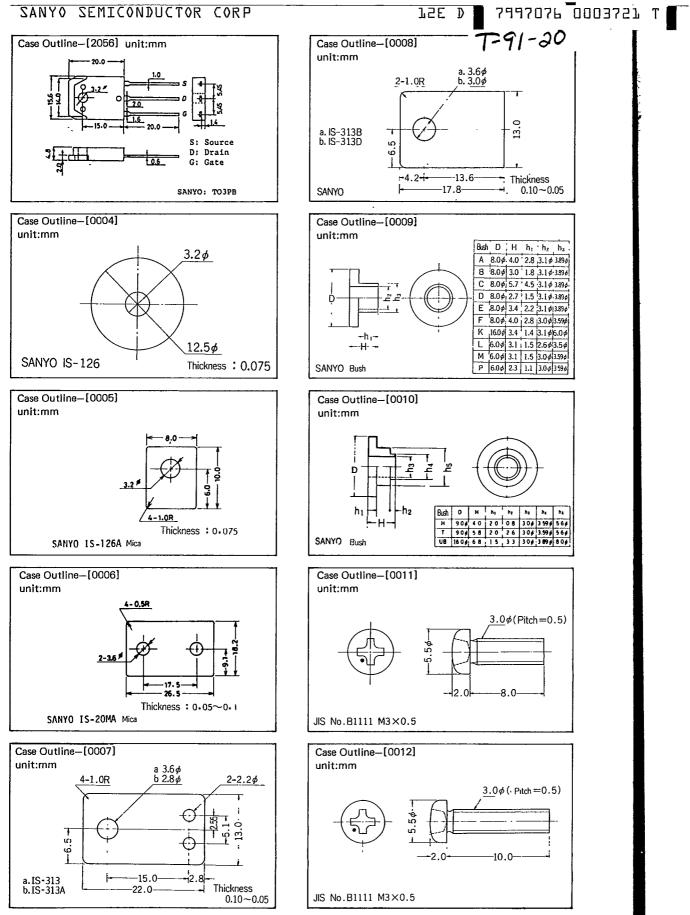
- •All of Sanyo Transistor case outlines are illustrated below.
- •All dimensions are in mm, and dimensions which are not followed by min. or max. are represented by typical values.
- •No marking is indicated.



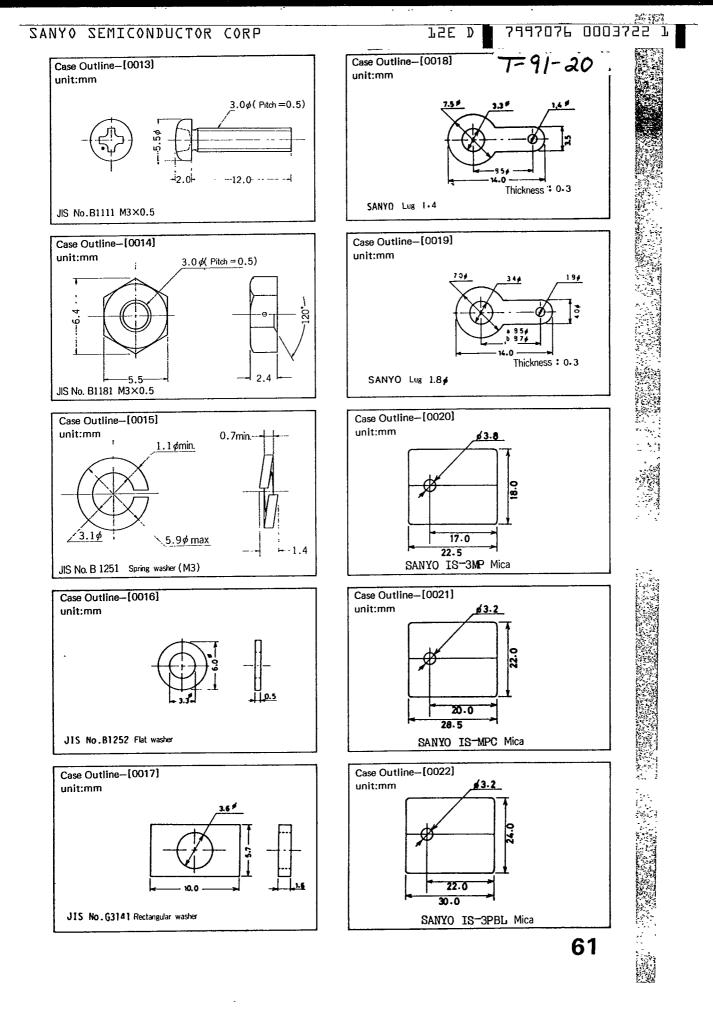












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