TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

# **TK20A60U**

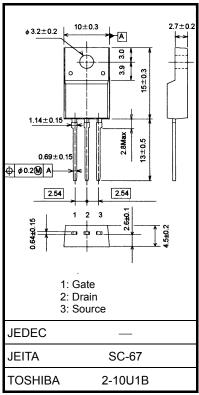
### **Switching Regulator Applications**

Unit: mm

- Low drain-source ON-resistance: RDS (ON) =  $0.165 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 12 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 100 \mu A (max) (V_{DS} = 600 V)$
- Enhancement-mode:  $V_{th} = 3.0 \text{ to } 5.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	600	V
Gate-source voltage		$V_{GSS}$	±30	V
Drain current	DC (Note 1)	I <sub>D</sub>	20	Α
	Pulse (Note 1)	I <sub>DP</sub>	40	A
Drain power dissipation	on (Tc = 25°C)	P <sub>D</sub>	45	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	144	mJ
Avalanche current		I <sub>AR</sub>	20	Α
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	4.5	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

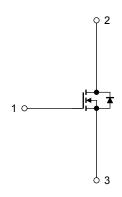
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 0.63 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 20 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Internal Connection



Start of commercial production 2008-05

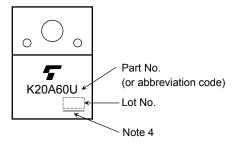
## **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	3.0	_	5.0	V
Drain-source ON	-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	_	0.165	0.19	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 A	3	12	_	S
Input capacitance		C <sub>iss</sub>		_	1470	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	150	_	
Output capacitance		C <sub>oss</sub>		_	3500	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 10 \text{ A}  V_{OUT} $	_	40	_	. ns
	Turn-on time	t <sub>on</sub>		_	80	_	
	Fall time	t <sub>f</sub>		_	12	_	
	Turn-off time	t <sub>off</sub>		_	100	_	
Total gate charge		Qg		_	27	_	
Gate-source charge		Qgs	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	_	16	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	11	_	

## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

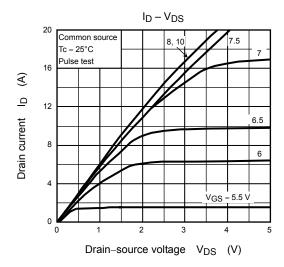
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	20	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	40	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 20 \text{ A}, V_{GS} = 0 \text{ V},$	_	450	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	8.1	_	μС

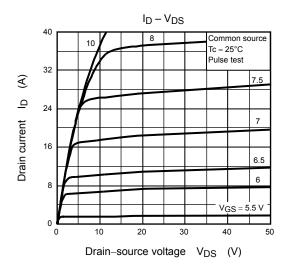
## Marking

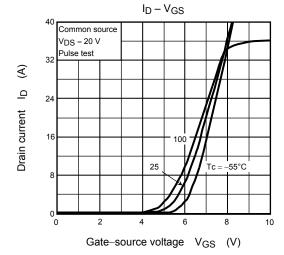


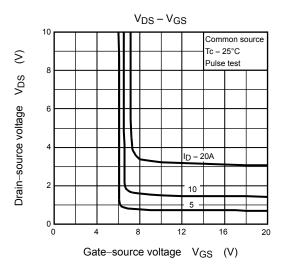
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

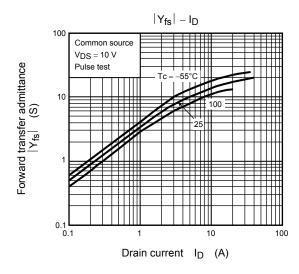
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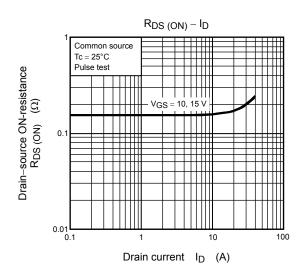


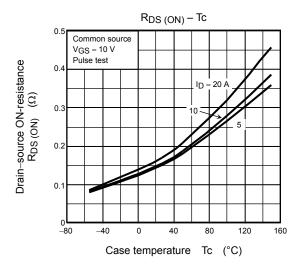


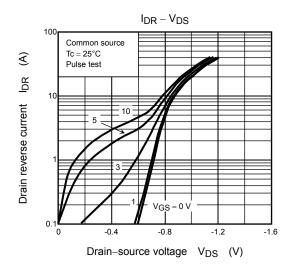


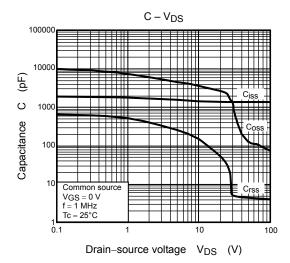


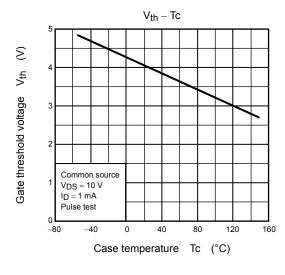


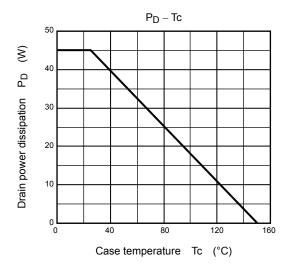


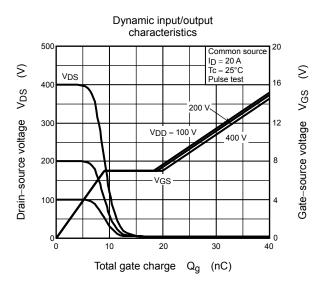


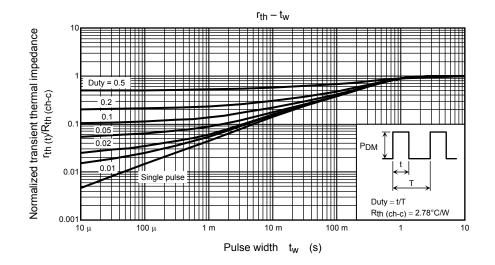


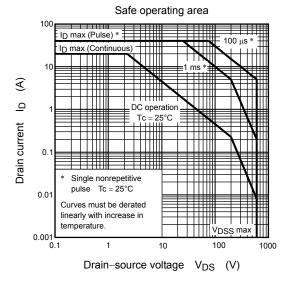


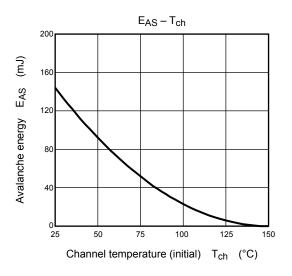


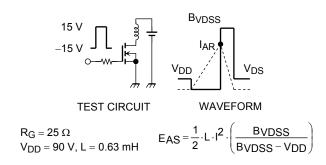












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