## **EPSON**

# CRYSTAL OSCILLATOR SG-8000 Series



1997

SEIKO EPSON CORP.

## Short Lead Time Crystal Oscillator EPSON's solution is SG-8000 to just in time (JIT)

Lead Time will be 1/3 compare to the previous, and 2 weeks lead times (ex. J)



What we have to do after receiving P/O is only to write the data to generic oscillator, it needs very short time. Our PLL & P•ROM technology makes it possible.



All our sells offices can program to meet customer's request for the immediate samples.



Professional operator is assigned for this job.



The data required from customers such as frequency, output level, and operating voltage.



Immediate response after receiving a inquiry.



Specific P•ROM writer and generic oscillators are provided at sales office and factory in advance.



ex.
New project must start urgently.
Sudden production ranp up.

Yes, We shall do!



How fast! Great!! Don't hesitate contact EPSON.

st! Within **2**Weeks

(min. 1 day)

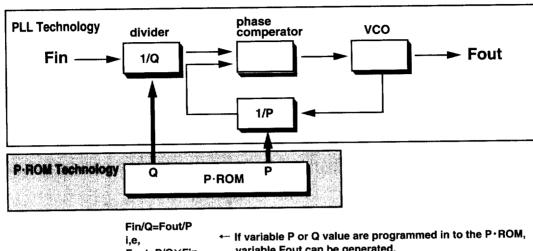


Yes, We can provide. When, how many pieces do you need?

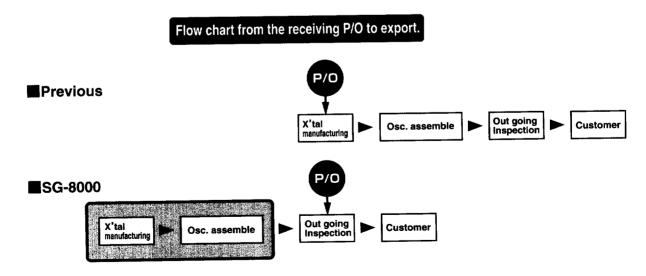


## SG-8000 is a most latest programmable oscillator.

1. Can program the frequency



- Fout=P/Q×Fin
- variable Fout can be generated.
- 2. Can program the output level (i, e, C-MOS level or TTL level)
- 3. Can program the operating voltage (i, e, 5 volt or 3.3 volt)
- ●Valuable packages are available (JA or JC is available now, Dip type coming soon)
- EPSON can deliver SG-8000 when you need, what frequency you need with short lead time.



SG-8000 consist of fundamental X'tal and custom IC including a PLL and P-ROM circuit.



#### Programmable Clock Oscillator

## SG - 8001JA (PH, PT, PC)

- Using PLL technology and P-ROM programmability for quick-turn custom version.
- Reflowable and high density mounting type SMD package compatible with SG-615.
- Operable 3.3V or 5.0V and out put frequencies from 2.5 MHz to 100 MHz at 5V (up to 80 MHz at 3.3V).
- Output enable function allow more low current consumption.

#### **Specifications**

1. Absolute Maximum Ratings

ttillen i en	Symbol	PH type PT type PC type	Remarks
Storage Temperature	Tstg	- 55 to 125℃	
Maximum supply voltage	VDD	- 0.5 to 7.0V	
Maximum input voltage	Vin	- 0.5 to VDD+0.5V	
Soldering condition	TSOL	Twice at under 260°C within 10sec.	
		or under 230°C within 3min.	

2. Operating Conditions

ltem :	Symbol	PH type	PT type	PC type	Remarks
Operating Temperature	TOPR		- 10 to 70℃		
Operating voltage	VDD	4.5	to 5.5V	3.0 to 3.6V	
Input voltage	Vin		GND to VD		
Output load condition	CL	30 pF			2.5 to 40MHz
발매용 연락됐다. 하라, 살길 : [		15 pF			2.5 to 100MHz
임근 함께 보는 사람들은 반별 등 보다.		2TTL+15pF			2.5 to 100MHz
				15pF	2.5 to 80MHz

3. Frequency Characteristics

ltem	Symbol	PH type PT type	PC type	Remarks
Output frequency range*	fo	2.5 to 100MHz	2.5 to 80MHz	
Frequency stability	f / fo	+/- <b>1</b> 00 pp	-10 to 70℃	
Aging	fa	+/- 5ppm m	ax.	

\*Note:

Please contact to EPSON about Standard frequency.



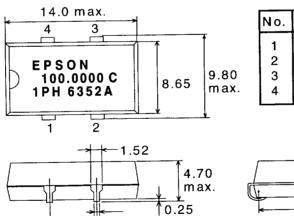
#### 4. Characteristics

Iten		Symbol	PH type	PT type	PC type	Remarks
Current consu	mption	IOP	45 mA	Max.	23 mA max.	
Disable currer		IOE	30 m <i>A</i>	Max.	16 mA max.	
OE input volta		Vін	2.0 V	min.	70%VDD	
		VIL	0.8 V	max.	20%VDD	
OE input curre	int 115 1	IIН	2.0 μ.	A max.	2.0 μA max.	OE= VDD
		lıL	16.0 µ.	A max.	7.0 μA max.	OE=GND
Duty		Tw/T	40 to 60%			50%VDD
				45 to 55%		1.4V, 2TTL (up to 80M)
				40 to 60%		1.4V, 2TTL ( 80 to 100M)
					40 to 60%	50%VDD ( up to 66.67M)
					35 to 55%	50%VDD ( 66.67 to 80M)
Output voltage	C-MOS	Vон	VDD-0.4		<b>V</b> DD-0.4	
Vollage		Vol	0.4V		0.4V	
	TTL	Vон		VDD-0.4		
		Vol		0.4V		
Output rise	C-MOS	tr	3.5 nS		3.5 nS	20% to 80%VDD CL=15pF
time			7.0 nS			20% to 80%VDD CL=30pF
THE STATE OF THE S		1		2.5 nS		0.4V to 2.4V 2TTL+15pF
			5.0 nS			0.4V to 2.4V 30pF
Output fall C-MOS time		tf	2.5 nS		2.5 nS	20% to 80%VDD CL=15pF
			5.0 nS			20% to 80%VDD CL=30pF
	ATTLE .	1		2.0 nS		0.4V to 2.4V 2TTL+15pF
				4.0 nS		0.4V to 2.4V 30pF
Oscillation sta	art up time	tosc		10 mSec. m	ax.	

\*\*Note:

Output wave form is not compatible with C-MOS level and TTL level. Programmable wave form only for C-MOS level or TTL level.

#### 5. External Dimensions (Unit: mm)



min.

0.51

Pin terminal

ΟE

GND

OUT

VDD



#### Programmable Clock Oscillator

## SG - 8001JC (PH, PT, PC)

- Using PLL technology and P-ROM programmability for quick-turn custom version.
- Reflowable and high density mounting type SMD package compatible with SG-636.
- Operable 3.3V or 5.0V and out put frequencies from 2.5 MHz to 80 MHz.
- Output enable function allow more low current consumption.

#### **Specifications**

1. Absolute Maximum Ratings

tem	Symbol	PH type   PT type   PC type	Remarks
Storage Temperature	Tstg	- 55 to 100℃	
Maximum supply voltage	VDD	- 0.5 to 7.0V	
Maximum input voltage	VIN	- 0.5 to VDD+0.5V	
Soldering condition	TsoL	Twice at under 260°C within 10sec.	
		or under 230℃ within 3min.	

2. Operating Conditions

tem :	Symbol	PH type	Pi type	PC type	Remarks
Operating Temperature	TOPR		- 10 to 70℃		
Operating voltage	VDD	4.5 1	to 5.5V	3.0 to 3.6V	
Input voltage	Vin	GND to VDD			
Output load condition	CL	30 pF			2.5 to 40MHz
하는 경기 : 사용하는 경기 (소통) 하는 경기 (요즘) - 1		15 pF			2.5 to 80MHz
어려면 시작되는 이 경험이 되는 물수 회복이다. 12. 하다는 사람들은 사람들이 아르네지 않는다.			2TTL+15pF		2.5 to 80MHz
				15pF	2.5 to 80MHz

3. Frequency Characteristics

the literal	Symbol	PH type PT type PC type	Remarks :
Output frequency range*	Fo	2.5 to 80MHz	
Frequency stability	F/Fo	+/- 100 ppm	-10 to 70℃
Aging	fa	+/- 5ppm max.	

<sup>\*</sup>Note:

Please contact to EPSON about Standard Frequency.



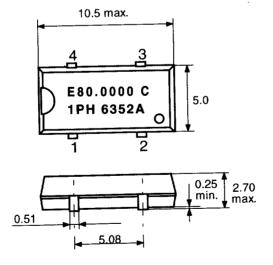
#### 4. Characteristics

. Characteris		Symbol	PH type	PT type	PC type	Remarks
Current consu		IOP	40 mA		23 mA max.	
Disable curren		IOE	27 mA max.		16 mA max.	
OE input volta	AND SOME STATE OF THE PROPERTY OF THE PARTY	VIH		min.	70%VDD	
OE INDUI VUKA	9 <b>5</b>	VIL		max.	20%VDD	
OE input curre		IIH		A max.	2.0 µA max.	OE= VDD
VE IIIPUL CULIE		lıL.		A max.	7.0 µA max.	OE=GND
		Tw/T	40 to 60%			50%VDD
Duty		1 **/		45 to 55%		1.4V, 2TTL+15pF
					40 to 60%	50%V <sub>DD</sub> (up to 66.67M)
					35 to 55%	50%VDD ( 66.67 to 80M)
Output G-MOS voltage	G-MOS	Vон	VDD-0.4		VDD-0.4	
		Vol	0.4V		0.4V	
	TIL	Voн		VDD-0.4		
		Vol		0.4V		
Output rise	C-MOS	tr	3.5 nS		3.5 nS	20% to 80%VDD CL=15pF 20% to 80%VDD CL=30pF
time		8	7.0 nS			0.4V to 2.4V 2TTL+15pF
	TIL			2.5 nS		0.4V to 2.4V 211L+13pF 0.4V to 2.4V 30pF
		7. 4.		5.0 nS		20% to 80%VDD CL=15pF
Output fall C-MC		tf	2.5 nS		2.5 nS	20% to 80% VDD CL=15pF
time		48998	5.0 nS			
	TIL	M		2.0 nS		0.4V to 2.4V 2TTL+15pF
				4.0 nS		0.4V to 2.4V 30pF
Oscillation sta	art up time	tosc		10 mSec. m	ax.	

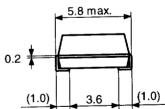
\*\*Note:

Output wave form is not compatible with C-MOS level and TTL level. Programmable wave form only for C-MOS level or TTL level.

## 5. External Dimensions ( Unit : mm )



No.	Pin terminal
1	OE
2	GND
3	OUT
4	VDD





### SG-8001JA vs SG-615

	SG-8001JA PH	SG-8001JA PT	SG-8001JA PC	SG-615P	SG-615PTJ	SG-615PH	SG-615PCV
Output Frequency Range	2.5 ~ 10	DO.OMHz	2.5 ~ 80.0MHz	1.025 ~ 26.0MHz	26.0 ~ 66	.6667MHz	40.0 ~ 125.0MHz
Maximum Supply Voltage		- 0.5 ~ + 7.0V	<u> </u>	- 0.3 ~ + 7.0V			- 0.5 ~ + 7.0V
Storage Temperature				- 55 ~ + 125℃			I
Soldering Temperature				x 10sec x 2 ti C x within 3 m			
Operating Voltage	4.5 ~	5.5 V	3.0 ~ 3.6V		4.5 ~	5.5 V	
Operating Temperature		VIII 00		-10 ~ +70℃			
Frequency Stability		C: ± 100 ppm			B: ± 50 ppm C: ± 100 ppm	C: ± 100 ppm	
Current Consumption	45	mA	23 mA	23 mA	35	50 mA	
Output Disable Current	30	mA	16 mA	12 mA	28 mA 20 mA		27 mA
DUTY	40% ~ 60%	45% ~ 55% (up t0 80M)	40% ~ 60% (up to 66.7 <b>M</b> )	40% ~ 60% (C-MOS)	45% ~ 55%	40% ~ 60%	35% ~ 60%
		40% ~ 60% (80 to 100M)	35% ~ 55% (66.7 to 80M)	45% ~ 55% ( TTL )			
Output Voltage(Voh)		VDD-0.4 V		VDD-0.4 V	2.4 V	VDD-0.4 V	VDD-0.5 V
Output Voltage(Vol)				0.4 V			
Output Load Condition	15 p <b>F</b>	2 TTL	15 pF	50 pF 10TTL	5 TTL	50 pF	25 pF
Input Voltage (High)	2.0	V	70%VDD	2.0 V	3.5 V	2.0 V	70%VDD
Input Voltage (Low)	3.0	3 V	20%VDD	0.8 V	1.5 V	0.8 V	20%VDD
Output Rise Time	3.5 nSec	2.5 nSec	3.5 nSec	8.0 nSec	5.0 nSec	7.0 nSec	4.0 nSec
Output Fall Time	2.5 nSec	2.0 nSec	2.5 nSec	8.0 nSec	5.0 nSec	7.0 nSec	4.0 nSec
Oscillation Start Time		10 mSec	100 3	4 mSec		10 mSec	
Aging				± 5 ppm			1.11.1



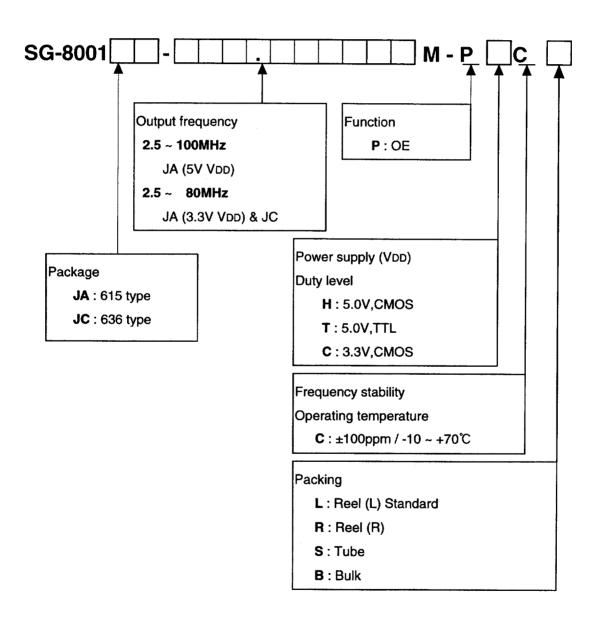
## SG-8001JC vs SG-636

	SG-8001JC PH	SG-8001JC PT	SG-8001JC PC	SG-636PTF	SG-636PTJ	SG-636PH	SG-636PCV		
utput Frequency Range	金生學·為主義而為。至1985-進出	2.5 ~ 80.0MHz		2.2167 ~ 41.0MHz	41.0 ~ 7	40.0 ~ 80.0MHz			
aximum Supply Voltage		- 0.5 ~ + 7.0V		- 0.3 ~ + 7.0V	- 0.5 ~ + 7.0V	- 0.3 ~ + 7.0V	- 0.5 ~ + 7.0\		
torage Temperature				- 55 ~ + 100℃					
oldering Temperature		260℃ x 10sec x 2 times or 230℃ x within 3 minute							
pperating Voltage	4.5 ~	5.5 V	3.0 ~ 3.6V		4.5 ~ 5.5 V		2.7 ~ 5.5 V		
Operating Temperature		-10 ~ +70℃							
requency Stability		C: ± 100 ppm							
Current Consumption	40 mA		23 mA	17 mA	35	mA	35 mA		
Output Disable Current	27	27 mA		10 mA	28 mA	20 mA	25 mA		
עדעכ	40% ~ 60%	45% ~ 55%	(up to 66.7M)			40% ~ 60%	40% ~ 60%		
			35% ~ 55% (66.7 to 80M)	45% ~ 55% (TTL)					
Output Voltage(Voh)		VDD-0.4 V		VDD-0.4 V	2.4 V	VDD-0.4 V	VDD-0.5 \		
Output Voltage(Vol)				0.4 V					
Output Load Condition	15 pF	2 TTL	15 pF	50 pF 10TTL	5 TTL	15 pF	25 pF		
Input Voltage (High)	2	2.0 V	70%VDD	2.0 V	3.5 V	2.0 V	70%VDE		
Input Voltage (Low)		).8 V	20%VDD	0.8 V	1.5 V	0.8 V	20%VDE		
Output Rise Time	3.5 nSec	2.5 nSec	3.5 nSec	7.0 nSec	5.0 nSec	5.0 nSec	4.0 nSec		
Output Fall Time	2.5 nSec	2.0 nSec	2.5 nSec	7.0 nSec	5.0 nSec	5.0 nSec	4.0 nSec		
Oscillation Start Time		10 mSec		4 mSec		10 mSec			
Aging				± 5 ppm	1				



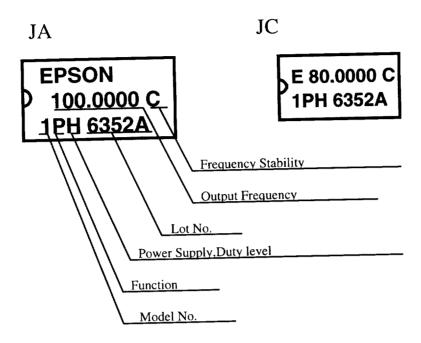
#### SG-8001 SAMPLE ORDERING FORMAT

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