MHO+ Series

14 pin DIP, 5.0 Volt, HCMOS/TTL, Clock Oscillator

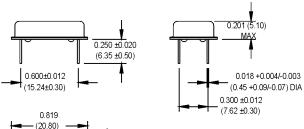


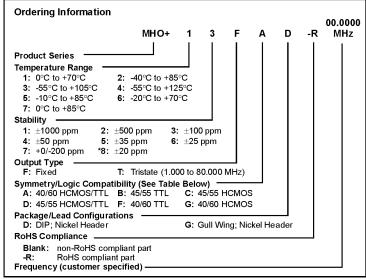




Features:

- Standard 14 DIP Package
- RoHS Compliant Version Available (-R)
- Tristate Option
- Wide Operating Temperature Range





^{*}Contact factory for availability

	0.819			
1	(20.80) MAX	_	7	All dimensions
	[©] O	0°	0.520	in inches (mm).
	•	್ಠ	(13.20) MAX	
14			8	
		<u>\</u>	INSULATED STA	ANDOFFS

Pin Connections

PIN	FUNCTION		
1	N/C or Tristate		
7	Circuit/Case Ground		
8	Output		
14	+Vdd		

Available Symmetry

FREQUENCY RANGE	STD.	OPTIONS
0.732 kHz to 50 MHz	Α	B, C, D
50.001 to 60 MHz	Α	B, C
60.001 to 67 MHz	Α	С
67.001 to 80 MHz	F,G	С

	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes
	Frequency Range	F	.732 kHz		80	Mhz	See Note 1
	Operating Temperature	TA	(See Order	(See Ordering Information)			
	Storage Temperature	Ts	-55		+125	°C	
	Frequency Stability	∆F/F	(See Order	(See Ordering Information)			
	Aging						
	1st Year			±3		ppm	
	Thereafter (per year)			±2		ppm	
	Input Voltage	Vdd	4.5	5.0	5.5	٧	
	Input Current	ldd			15	mA	.732 kHz to 2.999 MHz
S u					25	mA	3.000 to 25.999 MHz
aţio					60	mA	26.000 to 80.000 MHz
ific	Output Type						HCMOS/TTL
Sec	Load						See Note 2
<u> </u>				5 TTL or 50 pF			.732 kHz to 2.999 MHz
Electrical Specifications				10 TTL or 50 pF			3.000 to 67.000 MHz
			10 TTL or 1	10 TTL or 15 pF			67.001 to 80.000 MHz
	Symmetry (Duty Cycle)		(See Order	(See Ordering Information)			See Note 3
	Logic "1" Level	Voh	90% Vdd			V	HCMOS Load
			Vdd -0.5			V	TTL Load
	Logic "0" Level	Vol			10% Vdd	٧	HCMOS Load
					0.5	٧	TTL Load
	Output Current				±8	mA	0.732 kHz to 2.999 MHz
					±16	mA	3.000 to 80.000 MHz
	Rise/Fall Time	Tr/Tf					See Note 4
					20	ns	.732 kHz to 2.999 MHz
					10	ns	3.000 to 80.000 MHz
	Tristate Function		Input Logic "1" or floating; output active Input Logic "0"; output to high-Z				
	Start up Time			5		ms	
	Random Jitter	Rj		5	12	ps RMS	1-Sigma

- 1. Consult factory for availability of higher frequencies.
- 2. TTL load See load circuit diagram #1. HCMOS load See load circuit diagram #2.
- Symmetry is measured at 1.4 V with TTL load, and at 50% Vdd with HCMOS load.
 Rise/Fall times are measured between 0.5 V and 2.4 V with TTL load, and between 10% Vdd and 90% Vdd with HCMOS load.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.